

O I P E
APR 13 2004
JCGS
U.S. PATENT AND TRADEMARK OFFICE

Docket No.: M4065.0624/P624
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Howard Rhodes

Application No.: 10/751,439

Confirmation Number:

Filed: January 6, 2004

Art Unit: N/A

For: METHOD AND APPARATUS
PROVIDING CMOS IMAGER DEVICE
PIXEL WITH TRANSISTOR HAVING
LOWER THRESHOLD VOLTAGE THAN
OTHER IMAGER DEVICE TRANSISTORS

Examiner: Not Yet Assigned

SUBMISSION OF FORMAL DRAWINGS

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

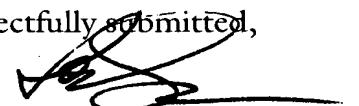
Dear Sir:

Submitted herewith is one set (nine sheets, twenty figures) of formal drawings for filing in the above-identified patent application. Kindly substitute the enclosed formal drawings for the informal drawings submitted with the originally filed application.

Dated: April 13, 2004

Respectfully submitted,

By _____


Thomas J. D'Amico
Registration No.: 28,371
Gabriela I. Coman
Registration No.: 50,515
DICKSTEIN SHAPIRO MORIN &
OSHINSKY LLP
2101 L Street NW
Washington, DC 20037-1526
(202) 785-9700
Attorney for Applicant